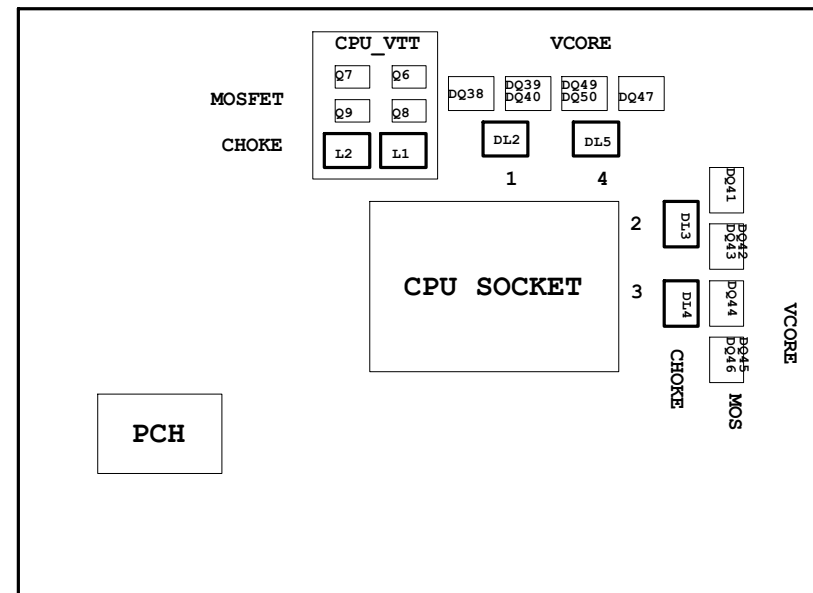


Model Name: GA-PA65-UD3-B3 1.0
SHEET
TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU LGA1155-A
05	CPU LGA1155-B
06	CPU LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH FDI,DMI,USB,PCIE
10	PCH DP,CLK BUFFER
11	PCH HOST,SATA,PCI
12	PCH GPIO,CTRL,AUDIO
13	PCH PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*1 SLOT &CLOCK BUFFER
16	ITE8892
17	PCI SLOT 1&2&3&4
18	I/O ITE8728
19	COM, R USB
20	Dual BIOS
21	ALC889
22	REAR AUDIO JACK
23	VCORE PWM ISL6364CRZ-1
24	VCORE PWM ISL6364CRZ-2
25	DISCRETE POWER
26	DDR 15V & VCC1 05 PCH PWM ISL6545CBZ
27	CPU VTT PWM ISL95870

SHEET
TITLE

28	VCCSA POWER
29	F PANEL , F USB
30	ATX POWER, CLOCK GEN
31	HWM,KB/MS , FAN CTRL
32	REALTEK RTL8111E
33	ETRON EJ168A R USB30
34	MARVEL 9172
35	ETRON EJ168A F USB30
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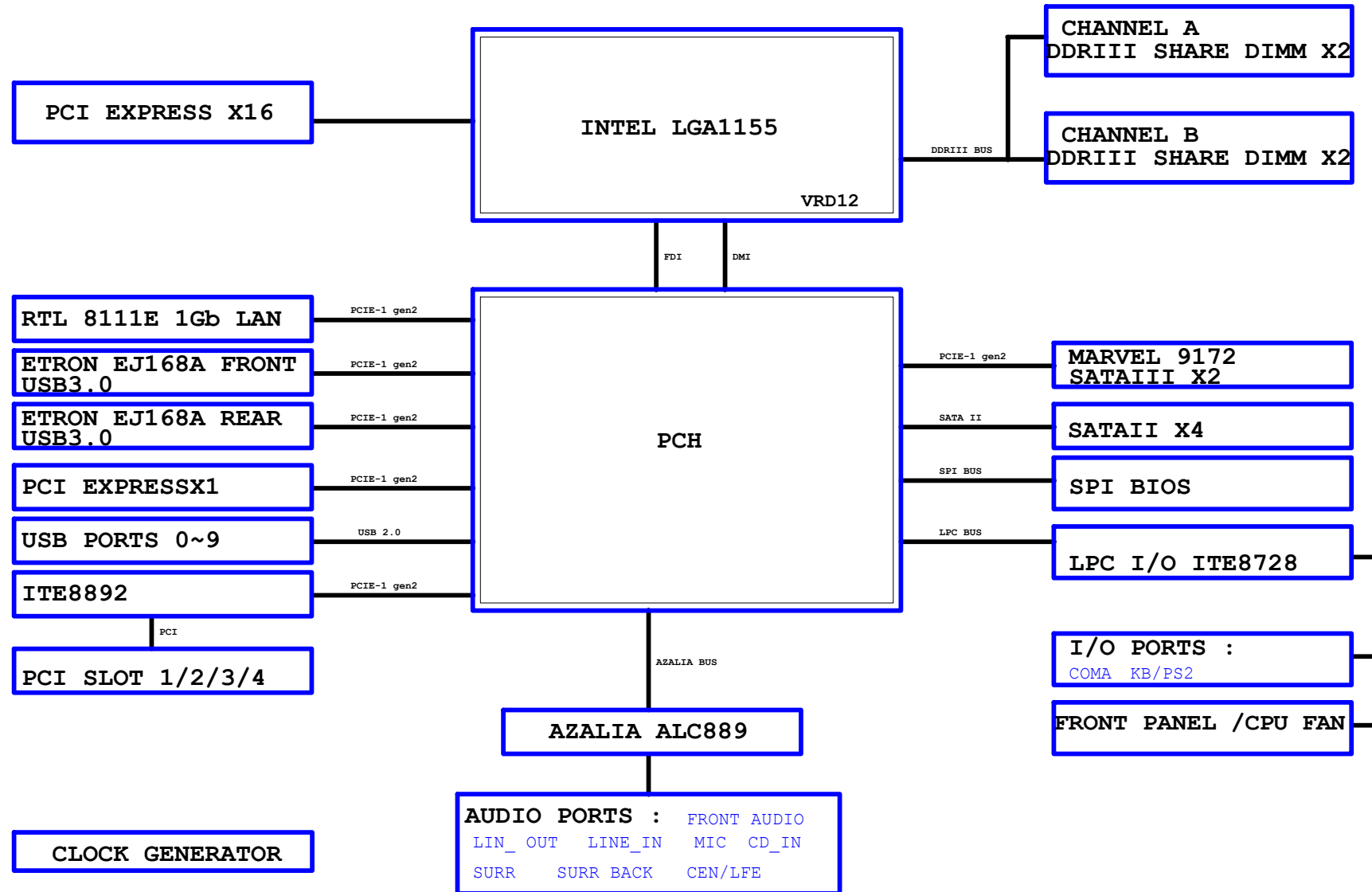

Gigabyte Technology

Title	Cover Sheet
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Custom	GA-PA65-UD3-B3
Date:	Tuesday, March 22, 2011
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Rev	1.0

A

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BLOCK DIAGRAM



LGA1155A

MAAA0	AV27	SA_MA[0]	AK3	DQSA0
MAAA1	AV24	SA_MA[1]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]		
MAAA3	AW23	SA_MA[3]		
MAAA4	AV23	SA_MA[4]	AJ3	MDA0
MAAA5	AT23	SA_MA[5]	AJ4	MDA1
MAAA6	AT24	SA_MA[6]	AL3	MDA2
MAAA7	AU22	SA_MA[7]	AL4	MDA3
MAAA8	AV22	SA_MA[8]	AJ2	MDA4
MAAA9	AT22	SA_MA[9]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	AL1	MDA7
MAAA12	AT21	SA_MA[12]		
MAAA13	AW32	SA_MA[13]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]		

7	-SWEA	-SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
7	-SCASA	-SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
7	-SRASA	-SRASA	AU28	SA_RAS#	SA_DQ[10]	AR3	MDA10
7	SBA00	SBA00	AV29	SA_BS[0]	SA_DQ[11]	AR4	MDA11
7	SBA01	SBA01	AW28	SA_BS[1]	SA_DQ[12]	AN2	MDA12
7	SBA02	SBA02	AV20	SA_BS[2]	SA_DQ[13]	AN3	MDA13
7	-CSA0	-CSA0	AU29	SA_CS[0]	SA_DQ[14]	AR2	MDA14
7	-CSA1	-CSA1	AV32	SA_CS[1]	SA_DQ[15]	AR1	MDA15
7	CKEA0	CKEA0	AV19	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
7	CKEA1	CKEA1	AT19	SA_CKE[1]	SA_DQ[17]	AW3	MDA17
7			AV18	SA_CKE[2]	SA_DQ[18]	AV5	MDA18
7			AV18	SA_CKE[3]	SA_DQ[19]	AW5	MDA19
7			AV31	SA_ODT[0]	SA_DQ[20]	AJ2	MDA20
7			AV32	SA_ODT[1]	SA_DQ[21]	AJ3	MDA21
7			AV33	SA_ODT[2]	SA_DQ[22]	AJ5	MDA22
7			AV33	SA_ODT[3]	SA_DQ[23]	AV5	MDA23

15	CLK_INTA0	CLK_INTA0	AY25	SA_CLK[0]	SA_DQ[24]	AV7	MDA24
15	-CLK_INTA0	-CLK_INTA0	AY25	SA_CLK[0]	SA_DQ[25]	AJ7	MDA25
15			AY26	SA_CLK[1]	SA_DQ[26]	AV9	MDA26
15			AY27	SA_CLK[2]	SA_DQ[27]	AV7	MDA27
15			AY28	SA_CLK[3]	SA_DQ[28]	AV7	MDA28
15			AY29	SA_CLK[4]	SA_DQ[29]	AV7	MDA29
15			AY30	SA_CLK[5]	SA_DQ[30]	AV9	MDA30
15			AY31	SA_CLK[6]	SA_DQ[31]	AV9	MDA31



AV13	SA_DQS[8]	SA_DQ[32]	AJ35	MDA32
AV12	SA_DQS[9]	SA_DQ[33]	AJ37	MDA33
AV12	SA_DQS[10]	SA_DQ[34]	AJ39	MDA34
AV12	SA_DQS[11]	SA_DQ[35]	AJ36	MDA35
AV12	SA_DQS[12]	SA_DQ[36]	AV35	MDA36
AV12	SA_DQS[13]	SA_DQ[37]	AV36	MDA37
AV12	SA_DQS[14]	SA_DQ[38]	AJ38	MDA38
AV12	SA_DQS[15]	SA_DQ[39]	AJ37	MDA39
AV12	SA_DQS[16]	SA_DQ[40]	AP38	DQSA5
AV12	SA_DQS[17]	SA_DQ[41]	AP39	-DQSA5

AV12	SA_DQS[18]	SA_DQ[42]	AR40	MDA40
AV12	SA_DQS[19]	SA_DQ[43]	AN37	MDA41
AV12	SA_DQS[20]	SA_DQ[44]	AN38	MDA42
AV12	SA_DQS[21]	SA_DQ[45]	AN37	MDA43
AV12	SA_DQS[22]	SA_DQ[46]	AR39	MDA44
AV12	SA_DQS[23]	SA_DQ[47]	AR38	MDA45
AV12	SA_DQS[24]	SA_DQ[48]	AN39	MDA46
AV12	SA_DQS[25]	SA_DQ[49]	AN40	MDA47
AV12	SA_DQS[26]	SA_DQ[50]	AK38	DQSA6
AV12	SA_DQS[27]	SA_DQ[51]	AK39	-DQSA6

AV12	SA_DQS[28]	SA_DQ[52]	AL40	MDA48
AV12	SA_DQS[29]	SA_DQ[53]	AJ37	MDA49
AV12	SA_DQS[30]	SA_DQ[54]	AJ38	MDA50
AV12	SA_DQS[31]	SA_DQ[55]	AJ37	MDA51
AV12	SA_DQS[32]	SA_DQ[56]	AL39	MDA52
AV12	SA_DQS[33]	SA_DQ[57]	AL38	MDA53
AV12	SA_DQS[34]	SA_DQ[58]	AJ39	MDA54
AV12	SA_DQS[35]	SA_DQ[59]	AJ40	MDA55
AV12	SA_DQS[36]	SA_DQ[60]	AF38	DQSA7
AV12	SA_DQS[37]	SA_DQ[61]	AF39	-DQSA7

AV12	SA_DQS[38]	SA_DQ[62]	AG40	MDA56
AV12	SA_DQS[39]	SA_DQ[63]	AG37	MDA57
AV12	SA_DQS[40]	SA_DQ[64]	AE38	MDA58
AV12	SA_DQS[41]	SA_DQ[65]	AE37	MDA59
AV12	SA_DQS[42]	SA_DQ[66]	AG39	MDA60
AV12	SA_DQS[43]	SA_DQ[67]	AG38	MDA61
AV12	SA_DQS[44]	SA_DQ[68]	AE39	MDA62
AV12	SA_DQS[45]	SA_DQ[69]	AE40	MDA63

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LGA1155B

MAAB0	AK24	SB_MA[0]	SB_DQS[0]	AH7	DQSB0
MAAB1	AM20	SB_MA[1]	SB_DQS[1]	AH6	-DQSB0
MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ8	MDB2
MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AN18	SB_MA[8]	SB_DQ[4]	AG6	MDB4
MAAB9	AV17	SB_MA[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MA[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MA[12]	SB_DQS[1]	AM8	DQSB1
MAAB13	AR28	SB_MA[13]	SB_DQS[1]	AL8	-DQSB1
MAAB14	AT18	SB_MA[14]			
MAAB15	AV16	SB_MA[15]			

8	-SWEB	-SWEB	AR29	SB_WE#	SB_DQ[8]	AL7	MDB8
8	-SCASB	-SCASB	AK29	SB_CAS#	SB_DQ[9]	AM7	MDB9
8	-SRASB	-SRASB	AP24	SB_RAS#	SB_DQ[10]	AM10	MDB10
8	SBA00	SBA00	AP24	SB_BS[0]	SB_DQ[11]	AL6	MDB11
8	SBA01	SBA01	AP24	SB_BS[1]	SB_DQ[12]	AM6	MDB12
8	SBA02	SBA02	AW17	SB_BS[2]	SB_DQ[13]	AL9	MDB13
8	-CSB0	-CSB0	AN25	SB_CS[0]	SB_DQ[14]	AM9	MDB14
8	-CSB1	-CSB1	AN26	SB_CS[1]	SB_DQ[15]	AM9	MDB15
8	CKEB0	CKEB0	AU16	SB_CKE[0]	SB_DQ[16]	AP7	MDB16
8	CKEB1	CKEB1	AY15	SB_CKE[1]	SB_DQ[17]	AR7	MDB17
8			AY15	SB_CKE[2]	SB_DQ[18]	AP10	MDB18
8			AY15	SB_CKE[3]	SB_DQ[19]	AR10	MDB19
8			AY15	SB_CKE[4]	SB_DQ[20]	AP6	MDB20
8			AY15	SB_CKE[5]	SB_DQ[21]	AR6	MDB21
8			AY15	SB_CKE[6]	SB_DQ[22]	AP9	MDB22
8			AY15	SB_CKE[7]	SB_DQ[23]	AP9	MDB23

8	MODT_B0	MODT_B0	AL26	SB_ODT[0]	SB_DQS[3]	AN13	DQSB3
8	MODT_B1	MODT_B1	AP28	SB_ODT[1]	SB_DQS[3]	AN12	-DQSB3
8			AP28	SB_ODT[2]	SB_DQS[3]		
8			AP28	SB_ODT[3]	SB_DQS[3]		
8	CLK_INTB0	CLK_INTB0	AL21	SB_CLK[0]	SB_DQ[24]	AM12	MDB24
8	CLK_INTB1	CLK_INTB1	AL22	SB_CLK[1]	SB_DQ[25]	AM13	MDB25
8	CLK_INTB2	CLK_INTB2	AL23	SB_CLK[2]	SB_DQ[26]	AP13	MDB26
8	CLK_INTB3	CLK_INTB3	AL24	SB_CLK[3]	SB_DQ[27]	AP13	MDB27
8	CLK_INTB4	CLK_INTB4	AL25	SB_CLK[4]	SB_DQ[28]	AL12	MDB28
8	CLK_INTB5	CLK_INTB5	AL26	SB_CLK[5]	SB_DQ[29]	AL12	MDB29
8	CLK_INTB6	CLK_INTB6	AL27	SB_CLK[6]	SB_DQ[30]	AL12	MDB30
8	CLK_INTB7	CLK_INTB7	AL28	SB_CLK[7]	SB_DQ[31]	AP12	MDB31

8	VREF_DQB	VREF_DQB	AH1	FC_AH1	SB_DQS[4]	AN29	DQSB4
8	VREF_DQA	VREF_DQA	AH4	FC_AH4	SB_DQS[4]	AN28	-DQSB4

8	AR28	MDB32	SB_DQ[32]	AR29	MDB33	SB_DQ[33]	AR29	MDB34
8	AR29	MDB34	SB_DQ[34]	AR29	MDB35	SB_DQ[35]	AR29	MDB36
8	AR30	MDB36	SB_DQ[36]	AR29	MDB37	SB_DQ[37]	AR29	MDB38
8	AR31	MDB38	SB_DQ[38]	AR29	MDB39	SB_DQ[39]	AR29	MDB40
8	AR32	MDB40	SB_DQ[40]	AR29	MDB41	SB_DQ[41]	AR29	MDB42
8	AR33	MDB42	SB_DQ[42]	AR29	MDB43	SB_DQ[43]	AR29	MDB44
8	AR34	MDB44	SB_DQ[44]	AR29	MDB45	SB_DQ[45]	AR29	MDB46
8	AR35	MDB46	SB_DQ[46]	AR29	MDB47	SB_DQ[47]	AR29	MDB48
8	AR36	MDB48	SB_DQ[48]	AR29	MDB49	SB_DQ[49]	AR29	MDB50
8	AR37	MDB50	SB_DQ[50]	AR29	MDB51	SB_DQ[51]	AR29	MDB52

8	AR38	MDB52	SB_DQ[52]	AR29	MDB53	SB_DQ[53]	AR29	MDB54
8	AR39	MDB54	SB_DQ[54]	AR29	MDB55	SB_DQ[55]	AR29	MDB56
8	AR40	MDB56	SB_DQ[56]	AR29	MDB57	SB_DQ[57]	AR29	MDB58
8	AR41	MDB58	SB_DQ[58]	AR29	MDB59	SB_DQ[59]	AR29	MDB60
8	AR42	MDB60	SB_DQ[60]	AR29	MDB61	SB_DQ[61]	AR29	MDB62
8	AR43	MDB62	SB_DQ[62]	AR29	MDB63	SB_DQ[63]	AR29	MDB64

8	AR44	MDB64	SB_DQ[64]	AR29	MDB65	SB_DQ[65]	AR29	MDB66
8	AR45	MDB66	SB_DQ[66]	AR29	MDB67	SB_DQ[67]	AR29	MDB68
8	AR46	MDB68	SB_DQ[68]	AR29	MDB69	SB_DQ[69]	AR29	MDB70
8	AR47	MDB70	SB_DQ[70]	AR29	MDB71	SB_DQ[71]	AR29	MDB72
8	AR48	MDB72	SB_DQ[72]	AR29	MDB73	SB_DQ[73]	AR29	MDB74
8	AR49	MDB74	SB_DQ[74]	AR29	MDB75	SB_DQ[75]	AR29	MDB76

8	AR50	MDB76	SB_DQ[76]	AR29	MDB77	SB_DQ[77]	AR29	MDB78
8	AR51	MDB78	SB_DQ[78]	AR29	MDB79	SB_DQ[79]	AR29	MDB80
8	AR52	MDB80	SB_DQ[80]	AR29	MDB81	SB_DQ[81]	AR29	MDB82
8	AR53	MDB82	SB_DQ[82]	AR29	MDB83	SB_DQ[83]	AR29	MDB84
8	AR54	MDB84	SB_DQ[84]	AR29	MDB85	SB_DQ[85]	AR29	MDB86
8	AR55	MDB86	SB_DQ[86]	AR29	MDB87	SB_DQ[87]	AR29	MDB88

8	AR56	MDB88	SB_DQ[88]	AR29	MDB89	SB_DQ[89]	AR29	MDB90
8	AR57	MDB90	SB_DQ[90]	AR29	MDB91	SB_DQ[91]	AR29	MDB92
8	AR58	MDB92	SB_DQ[92]	AR29	MDB93	SB_DQ[93]	AR29	MDB94
8	AR59	MDB94	SB_DQ[94]	AR29	MDB95	SB_DQ[95]	AR29	MDB96
8	AR60	MDB96	SB_DQ[96]	AR29	MDB97	SB_DQ[97]	AR29	MDB98
8	AR61	MDB98	SB_DQ[98]	AR29	MDB99	SB_DQ[99]	AR29	MDB100

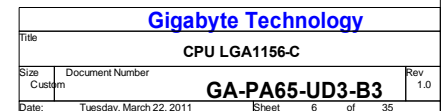
8	AR62	MDB100	SB_DQ[100]	AR29	MDB101	SB_DQ[101]	AR29	MDB102
8	AR63	MDB102	SB_DQ[102]	AR29	MDB103	SB_DQ[103]	AR29	MDB104
8	AR64	MDB104	SB_DQ[104]	AR29	MDB105	SB_DQ[105]	AR29	MDB106
8	AR65	MDB106	SB_DQ[106]	AR29	MDB107	SB_DQ[107]	AR29	MDB108
8	AR66	MDB108	SB_DQ[108]	AR29	MDB109	SB_DQ[109]	AR29	MDB110
8	AR67	MDB110	SB_DQ[110]	AR29	MDB111	SB_DQ[111]	AR29	MDB112

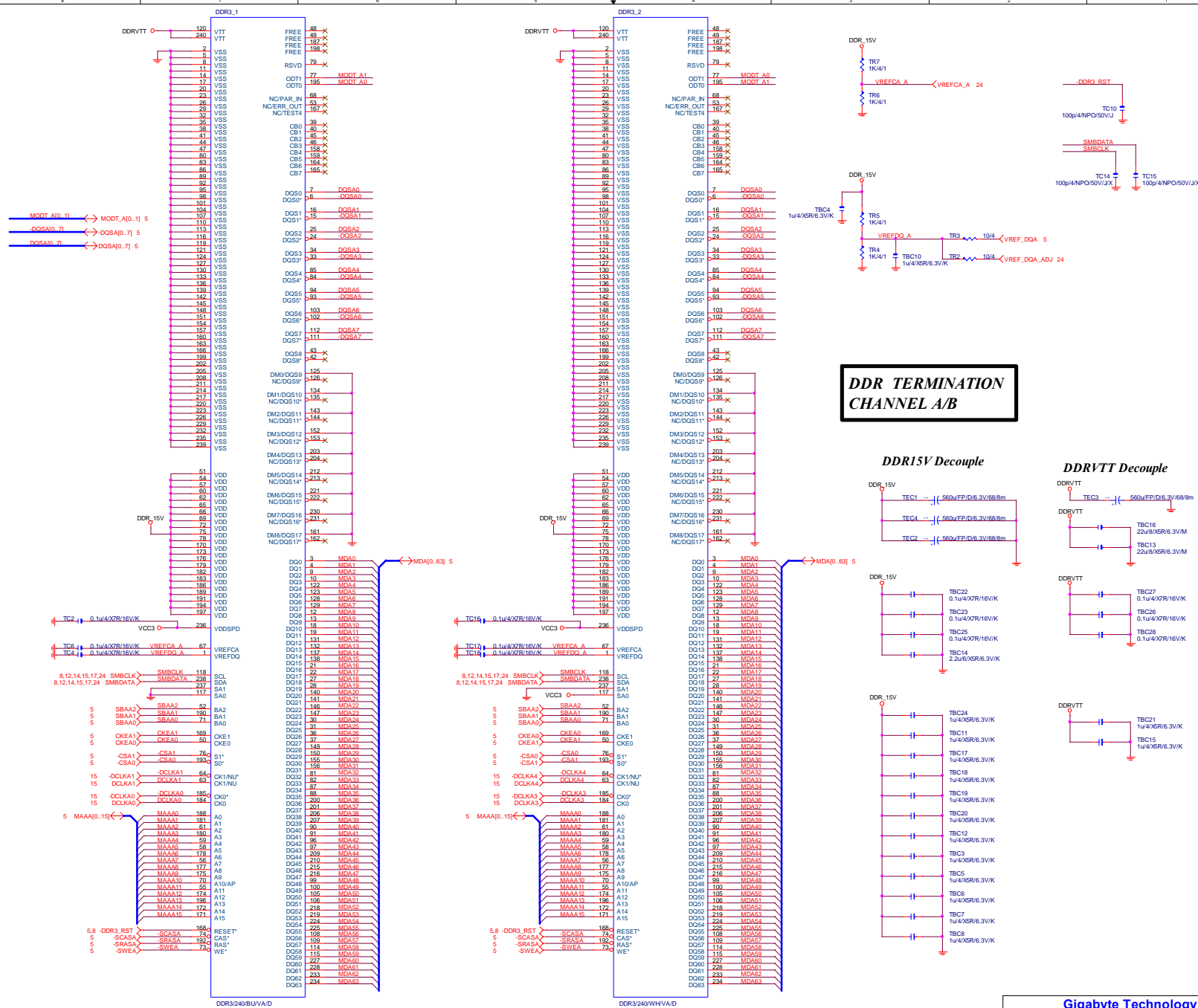
8	AR68	MDB112	SB_DQ[112]	AR29	MDB113	SB_DQ[113]	AR29	MDB114
8	AR69	MDB114	SB_DQ[114]	AR29	MDB115	SB_DQ[115]	AR29	MDB116
8	AR70	MDB116	SB_DQ[116]	AR29	MDB117	SB_DQ[117]	AR29	MDB118
8	AR71	MDB118	SB_DQ[118]	AR29	MDB119	SB_DQ[119]	AR29	MDB120
8	AR72	MDB120	SB_DQ[120]	AR29	MDB121	SB_DQ[121]	AR29	MDB122
8	AR73	MDB122	SB_DQ[122]	AR29	MDB123	SB_DQ[123]	AR29	MDB124

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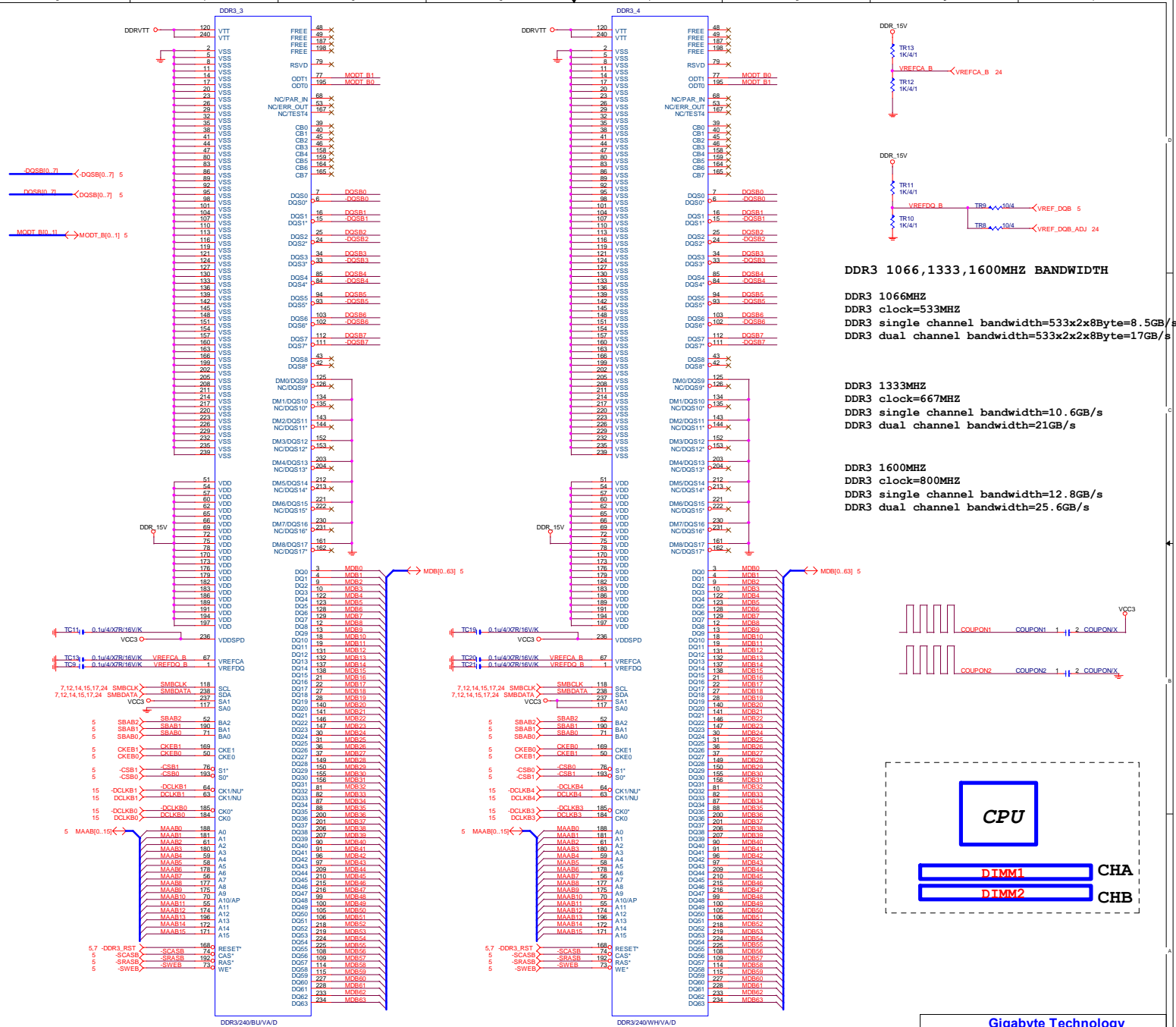
DDR TERMINATION CHANNEL A/B

DDR15V Decouple

DDRVTT Decouple

Gigabyte Technology

Gigabyte Technology			
Title			
DDRIII CHANNEL A			
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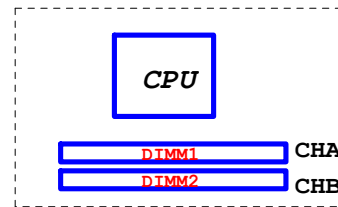


DDR3 1066,1333,1600MHZ BANDWIDTH

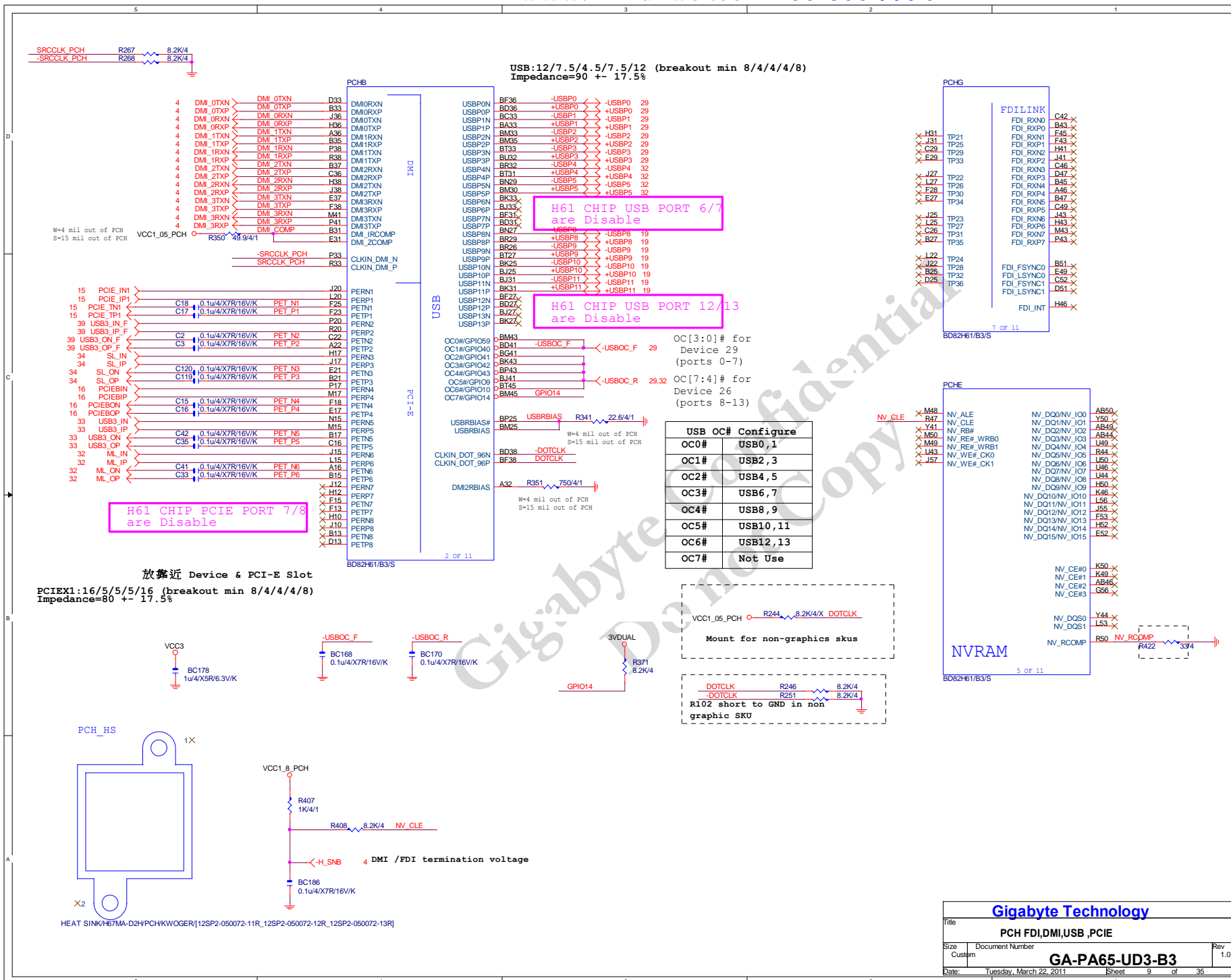
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DDR3 1066MHZ
DDR3 clock=533MHZ
DDR3 single channel bandwidth=533x2x8Byte=8.5GB/s
DDR3 dual channel bandwidth=533x2x2x8Byte=17GB/s
```

```
DDR3 1333MHZ
DDR3 clock=667MHZ
DDR3 single channel bandwidth=10.6GB/s
DDR3 dual channel bandwidth=21GB/s
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```
DDR3 1600MHZ
DDR3 clock=800MHZ
DDR3 single channel bandwidth=12.8GB/s
DDR3 dual channel bandwidth=25.6GB/s
```

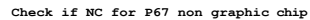


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Title <div style="text-align: center;">DDRIII CHANNEL B</div>			
Size Custom	Document Number <div style="text-align: center;">GA-PA65-UD3-B3</div>		Page 1
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SATA:20/4.5/7.5/4.5/20 (breakout min 8/4/4/8)
Impedance=90 +- 17.5%

PCHC

PCHA

PCHB

PCHD

PCH5

PCH6

PCH7

PCH8

PCH9

PCH10

PCH11

PCH12

PCH13

PCH14

PCH15

PCH16

PCH17

PCH18

PCH19

PCH20

PCH21

PCH22

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PCH333

PCH334

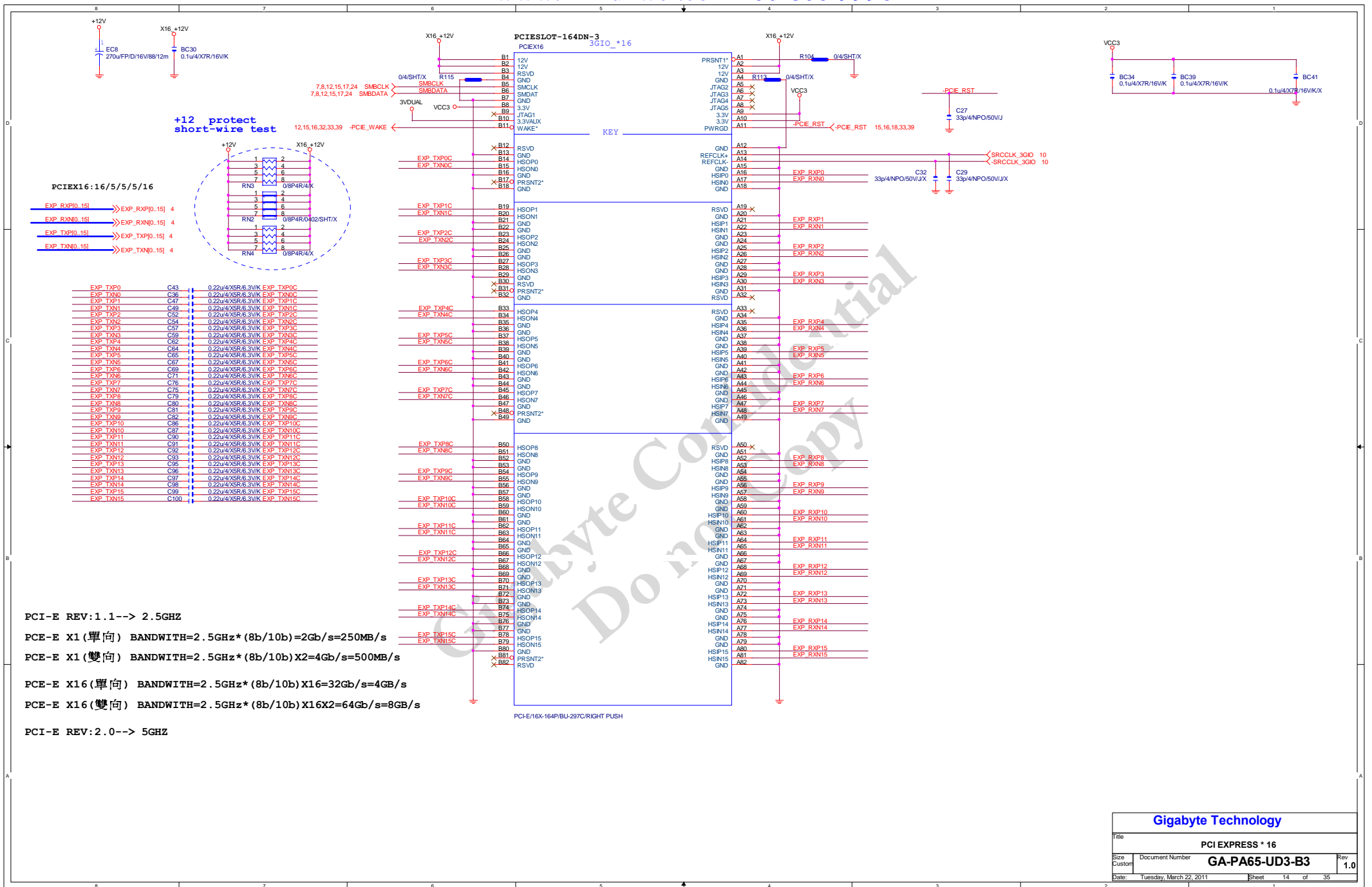
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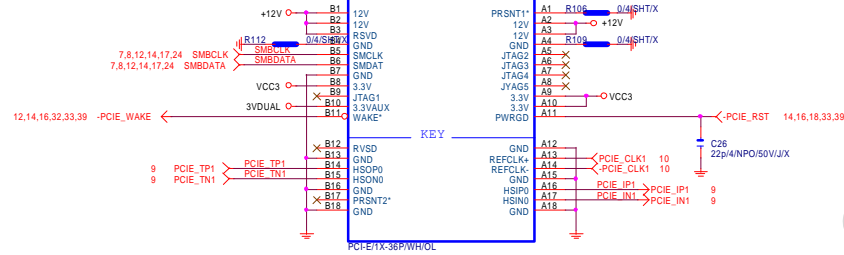


NOTICE

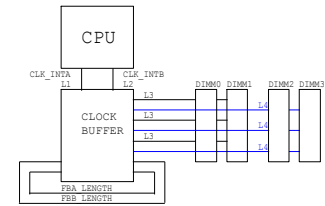
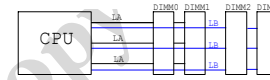
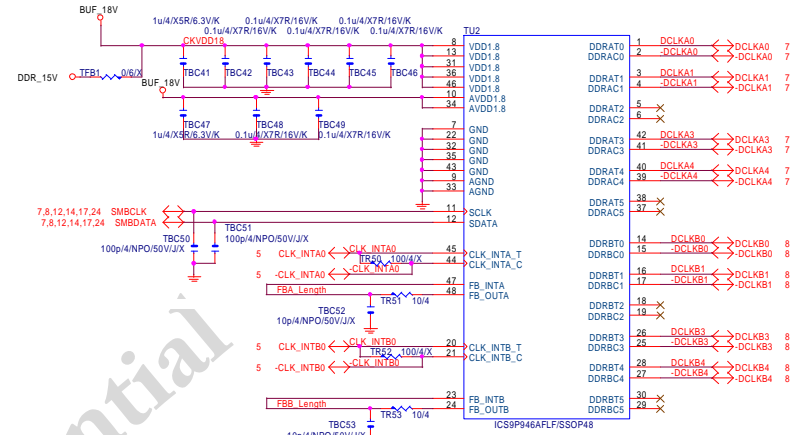
FBA_LENGTH=L1+L3-LA
 FBB_LENGTH=L2+L4-LB
 $DCLKA_SPEC(AVG) = (2.0 + 3.25) / 2 = 2.60 [in]$
 $DCLKB_SPEC(AVG) = (3.0 + 4.25) / 2 = 3.60 [in]$
 $FBA_Length = [CLK_INTA(MAX) + DCLKA(MAX)] - [DCLKA_SPEC(AVG)]$
 $FBB_Length = [CLK_INTB(MAX) + DCLKB(MAX)] - [DCLKB_SPEC(AVG)]$
 $FBA(3700) = [CLK_INTA(3700) + DCLKA(2700)] - DCLKA_SPEC(2700)$
 $FBB(4500) = [CLK_INTB(4400) + DCLKB(3700)] - [DCLKB_SPEC(3600)]$

PCIE*1

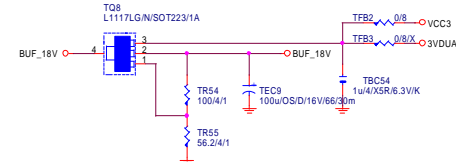
3GIO_X1



DDR3 CLOCK BUFFER

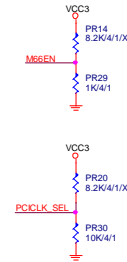
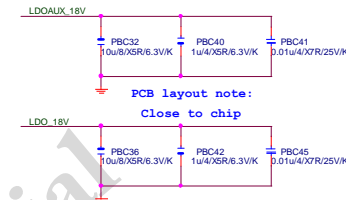
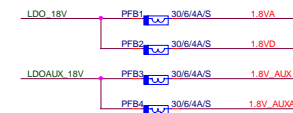
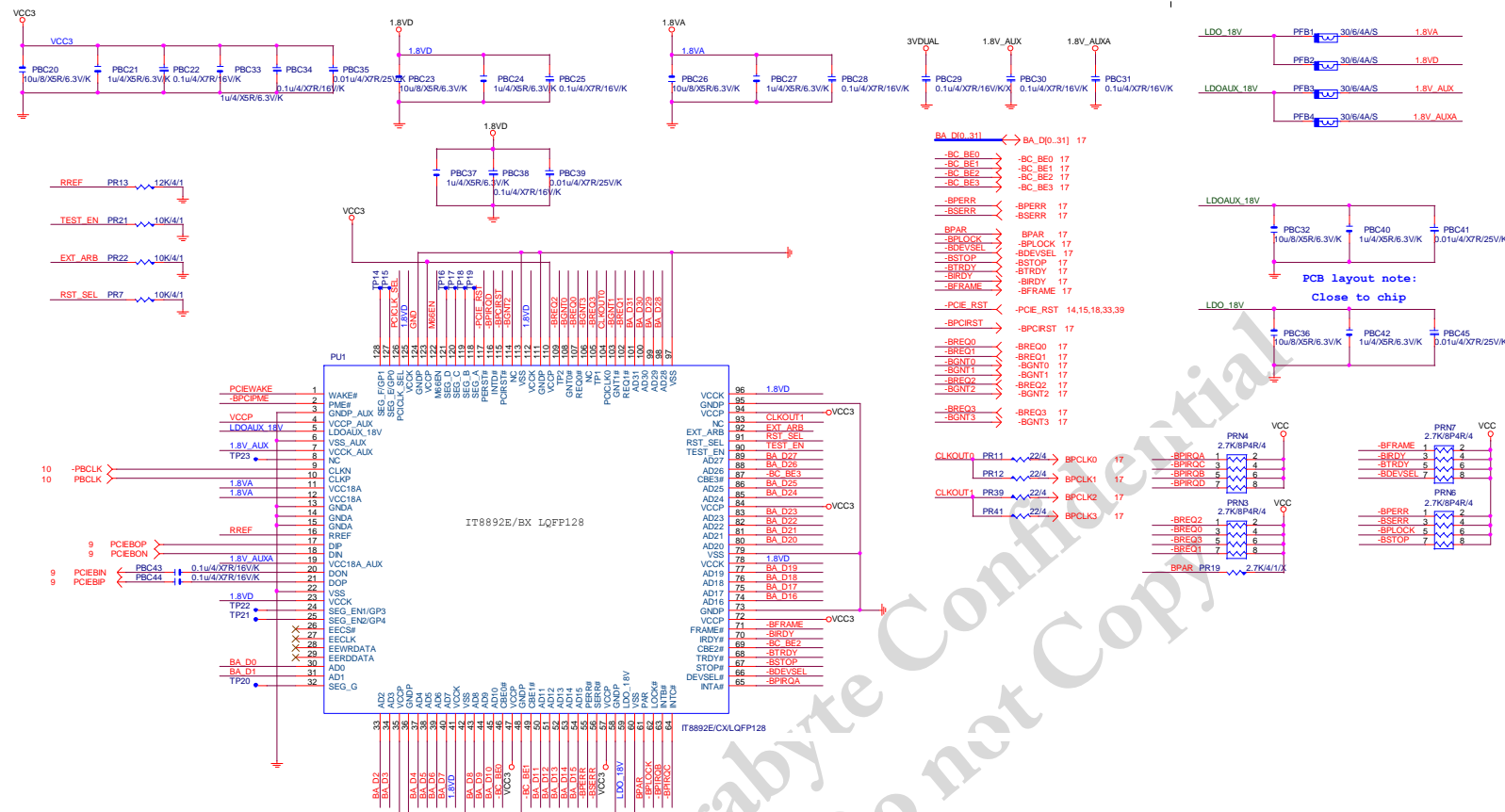


3.3V to 1.8V Voltage Regulator



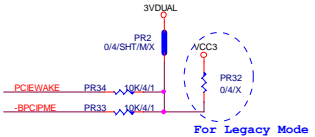
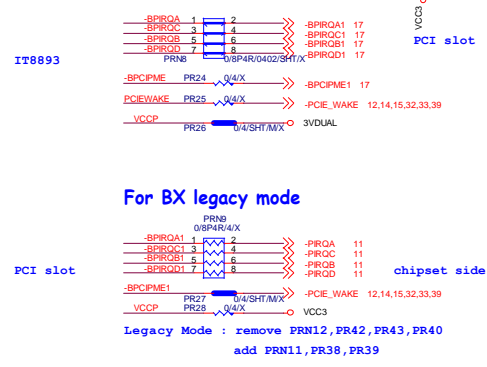
Gigabyte Technology

File			
PCI EXPRESS X 1 & Clock buffer			
Size	Document Number	Rev	
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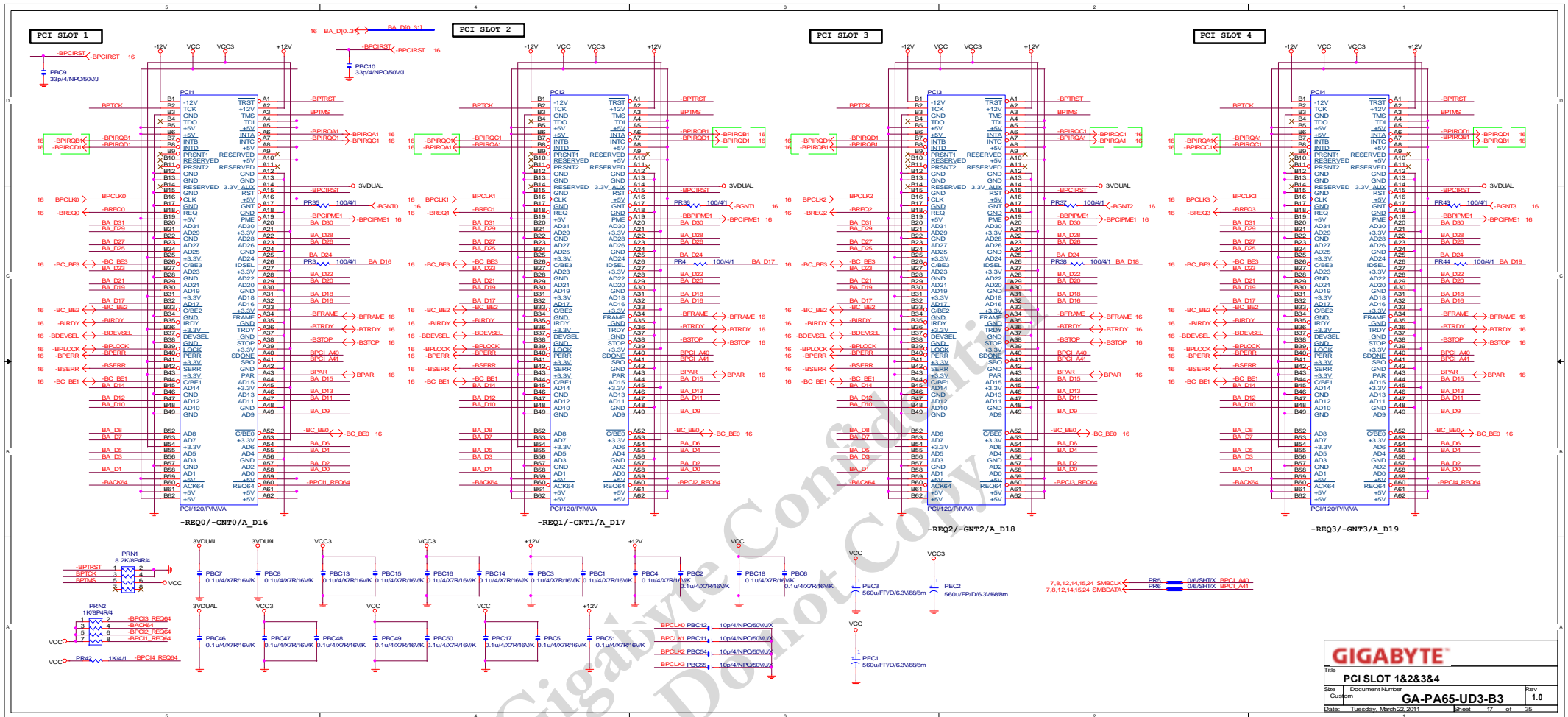


High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz

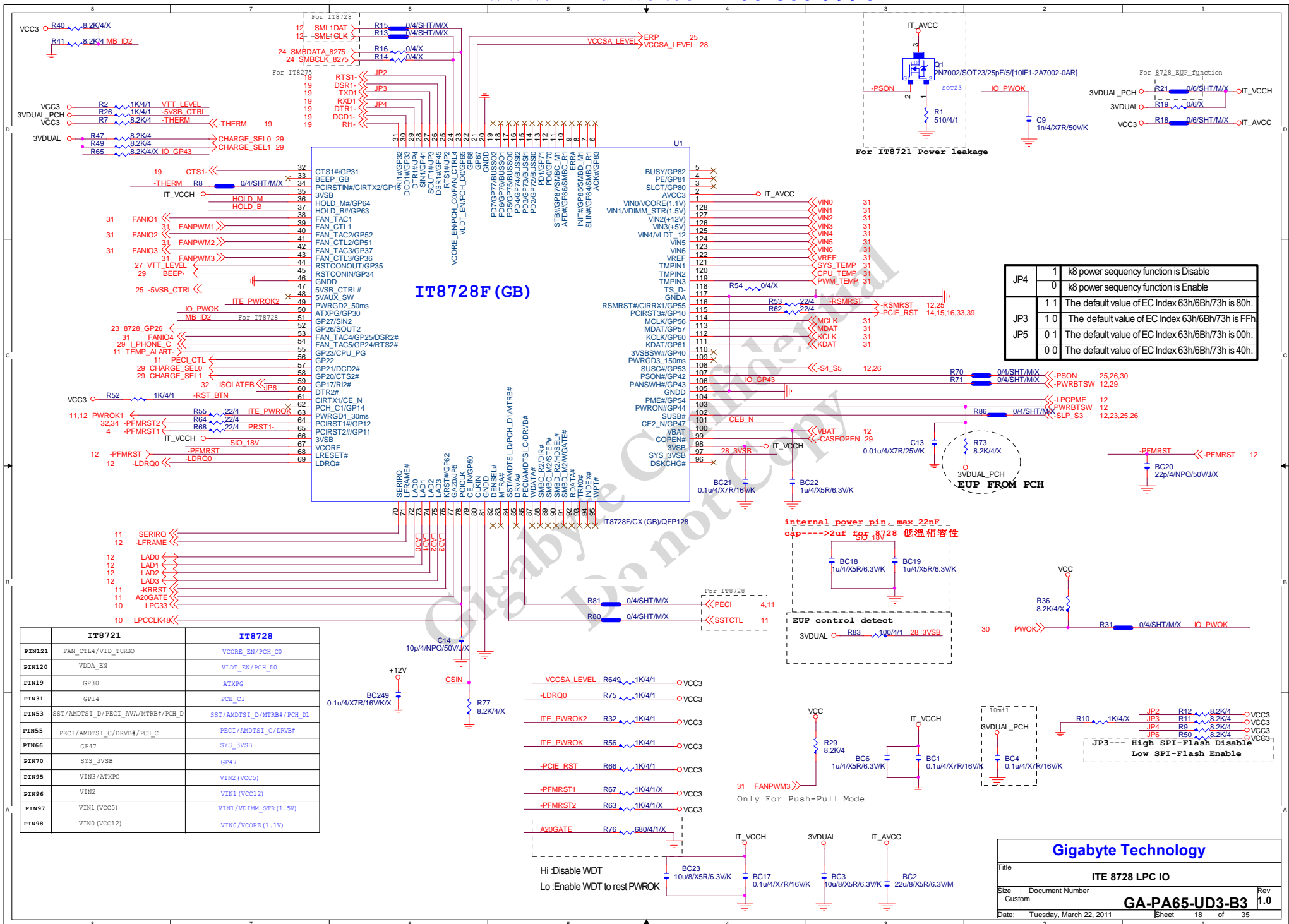
High: PCICLK INPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip



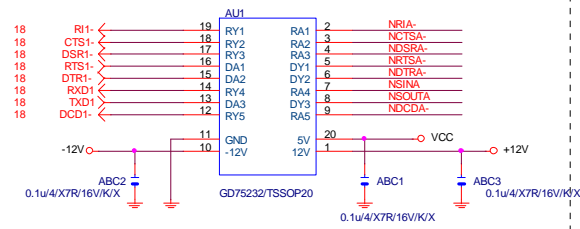
Gigabyte Technology			
ITE8892			
Sub Custom	Document Number	Rev	1.0
GA-PA65-UD3-B3			
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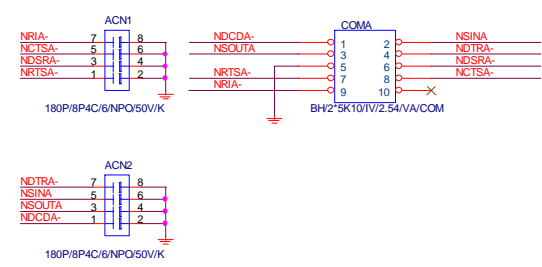
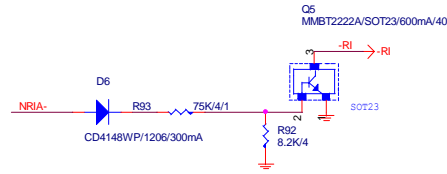
GIGABYTE®		
PCI SLOT 1&2&3&4		
Doc	Document Number	Rev
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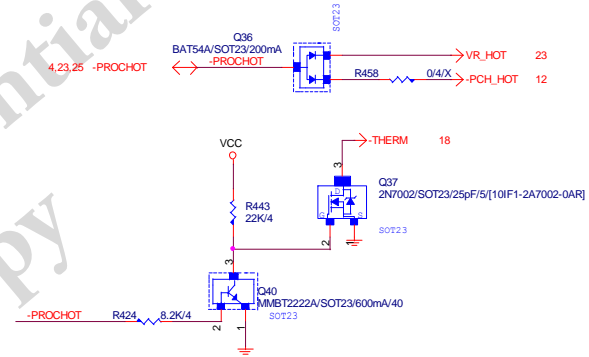
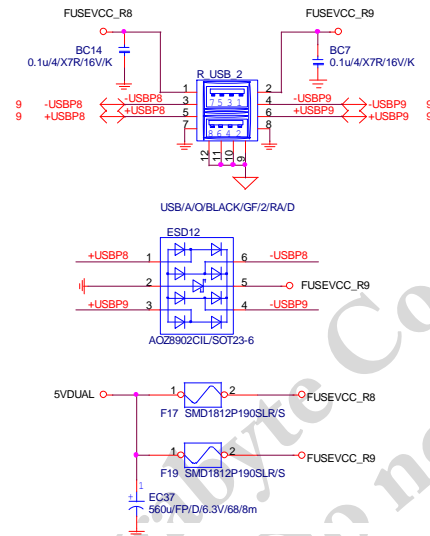
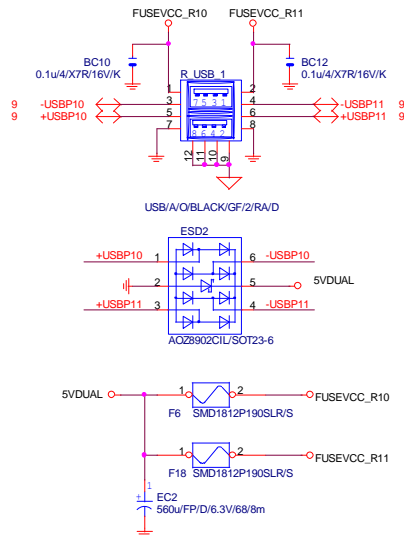
COMA



COM RI

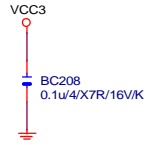


R_USB



Gigabyte Technology

Title		COM & R_USB	
Size	Document Number	GA-PA65-UD3-B3	
Custom		Rev 1.0	
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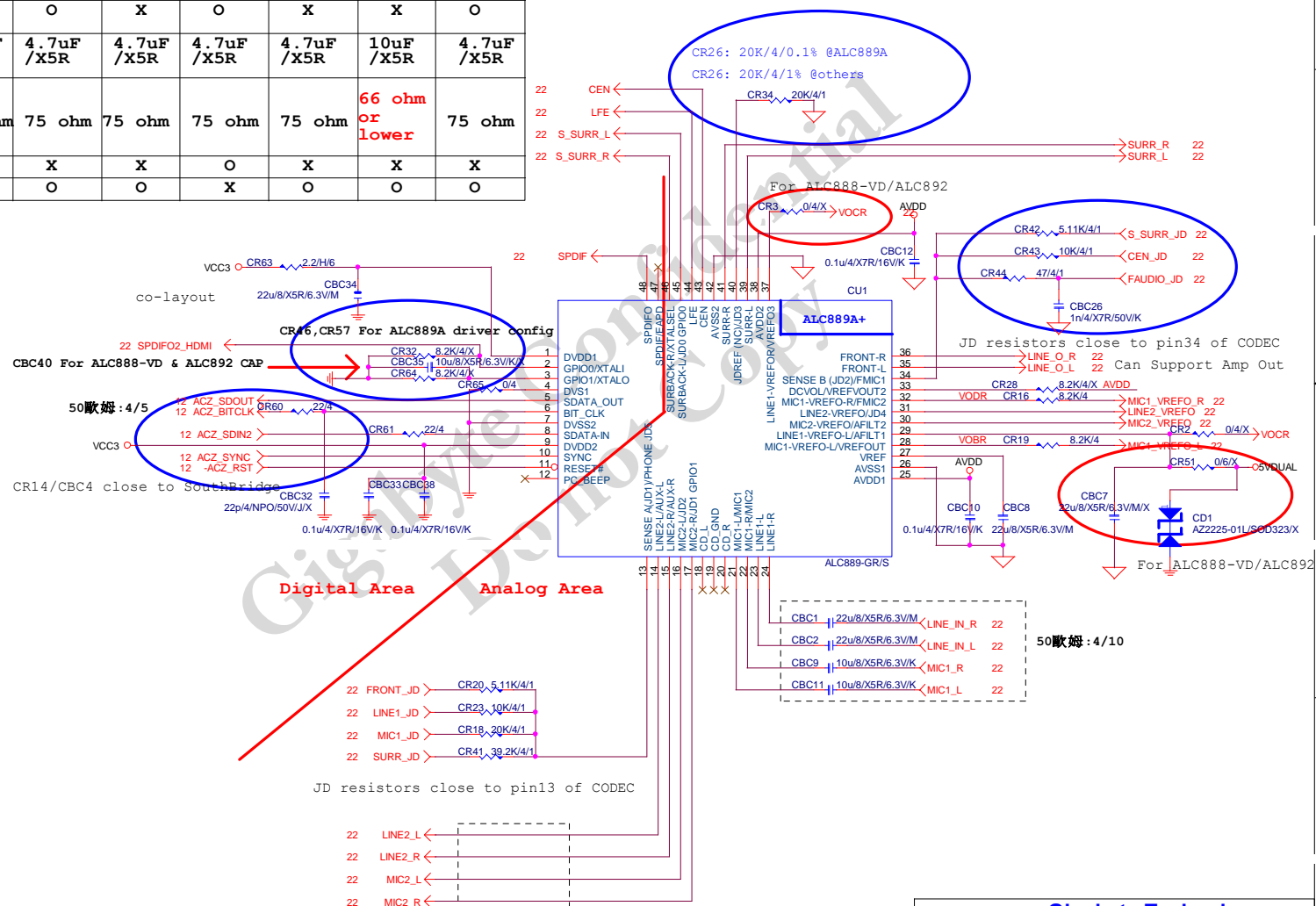


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Do not Copy

Gigabyte Technology			
Title		BIOS	
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		2	1

ALC883/ALC888-A/ALC888B/ALC888-VD/ALC892R/ALC889/ALC889A Colay

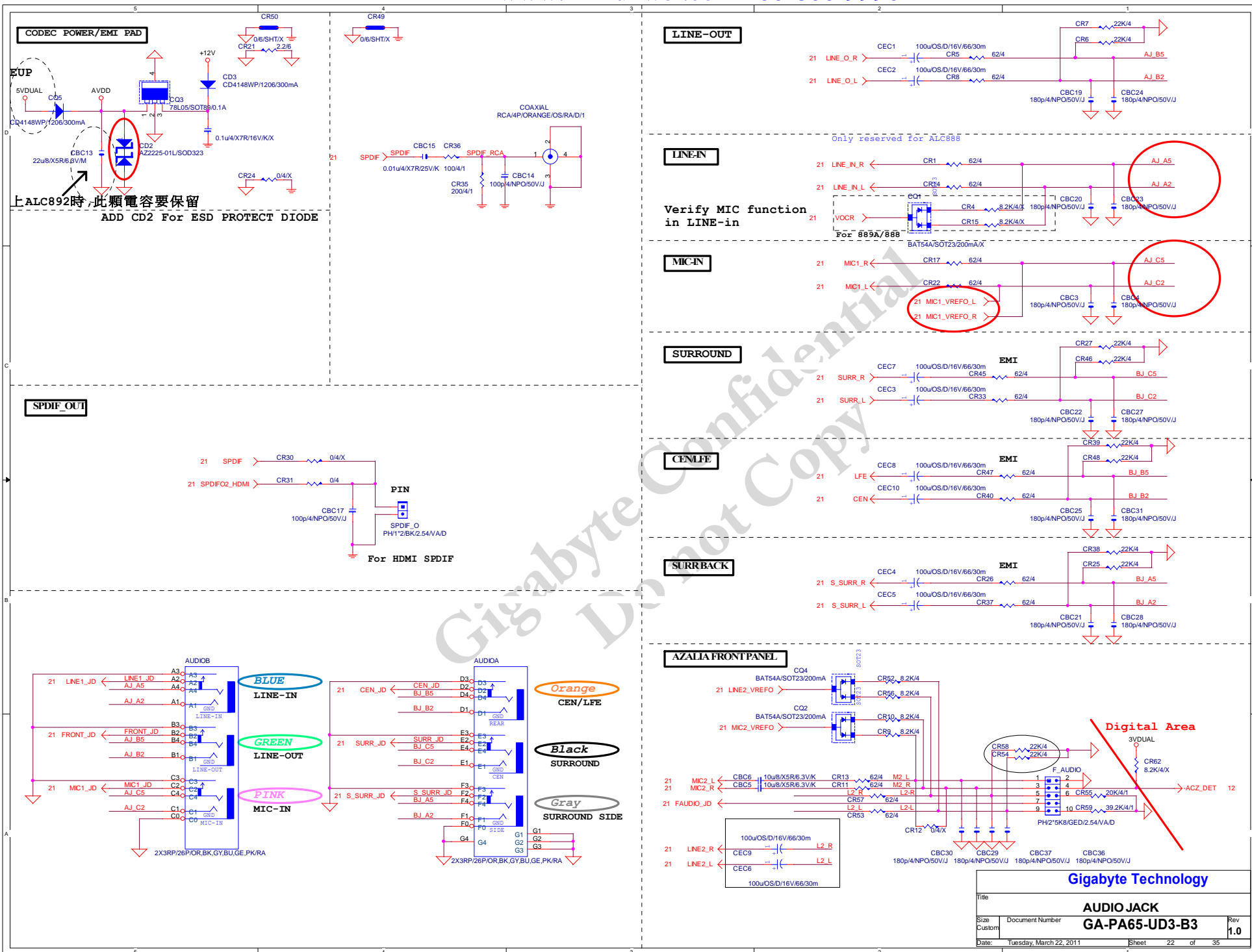
	ALC883	ALC888-VA	ALC888B	ALC888-VD	ALC892R	ALC889	ALC889A
CR32	X	X	X	X	X	X	O
CR64	X	X	X	X	X	X	O
CR65	O	O	X	X	X	O	O
CBC35	X	X	X	10uF/X5R	10uF/X5R	X	X
CR28	O	X	X	X	X	X	X
CR34	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/0.1%
CR31	X	X	O	X	O	O	X
CR30	O	O	X	O	X	X	O
CBC1/CBC2/CBC5/ CBC6/CBC9/CBC11	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	10uF /X5R	4.7uF /X5R
CR5/CR8/CR11/CR4/ CR17/CR22/CR45/CR33/ CR47/CR40/CR26/CR37/ CR13/CR11/CR57/CR53	75 ohm	75 ohm	75 ohm	75 ohm	75 ohm	66 ohm or lower	75 ohm
CR51/CD1/CBC7	X	X	X	O	X	X	X
CD2/CD3/CQ3/CQ5	O	O	O	X	O	O	O

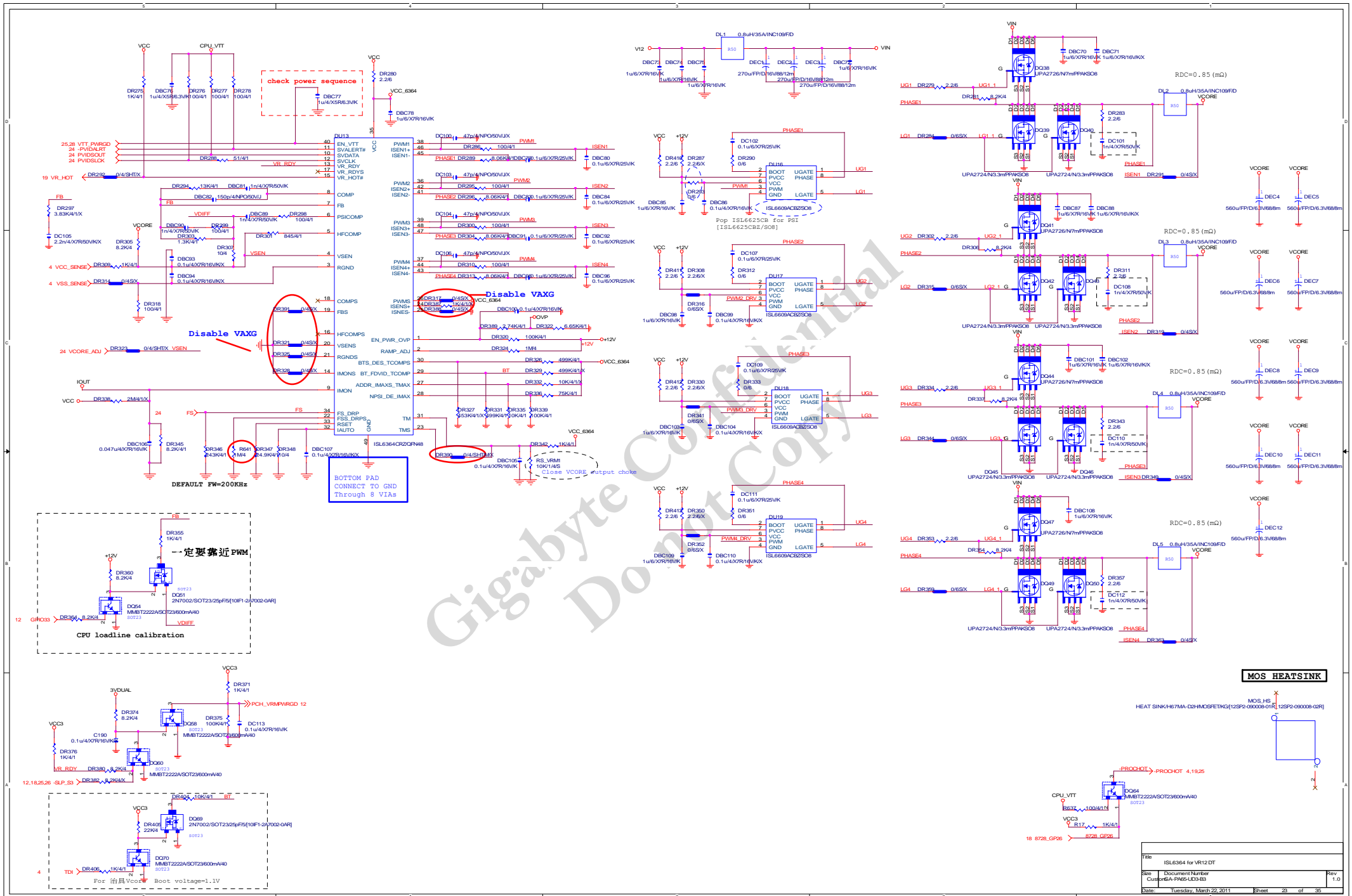


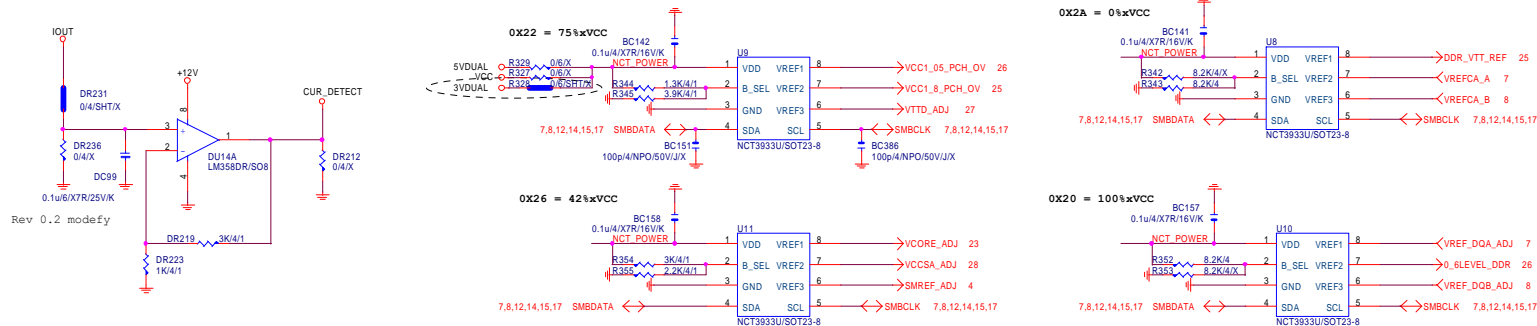
Can Support Amp Out

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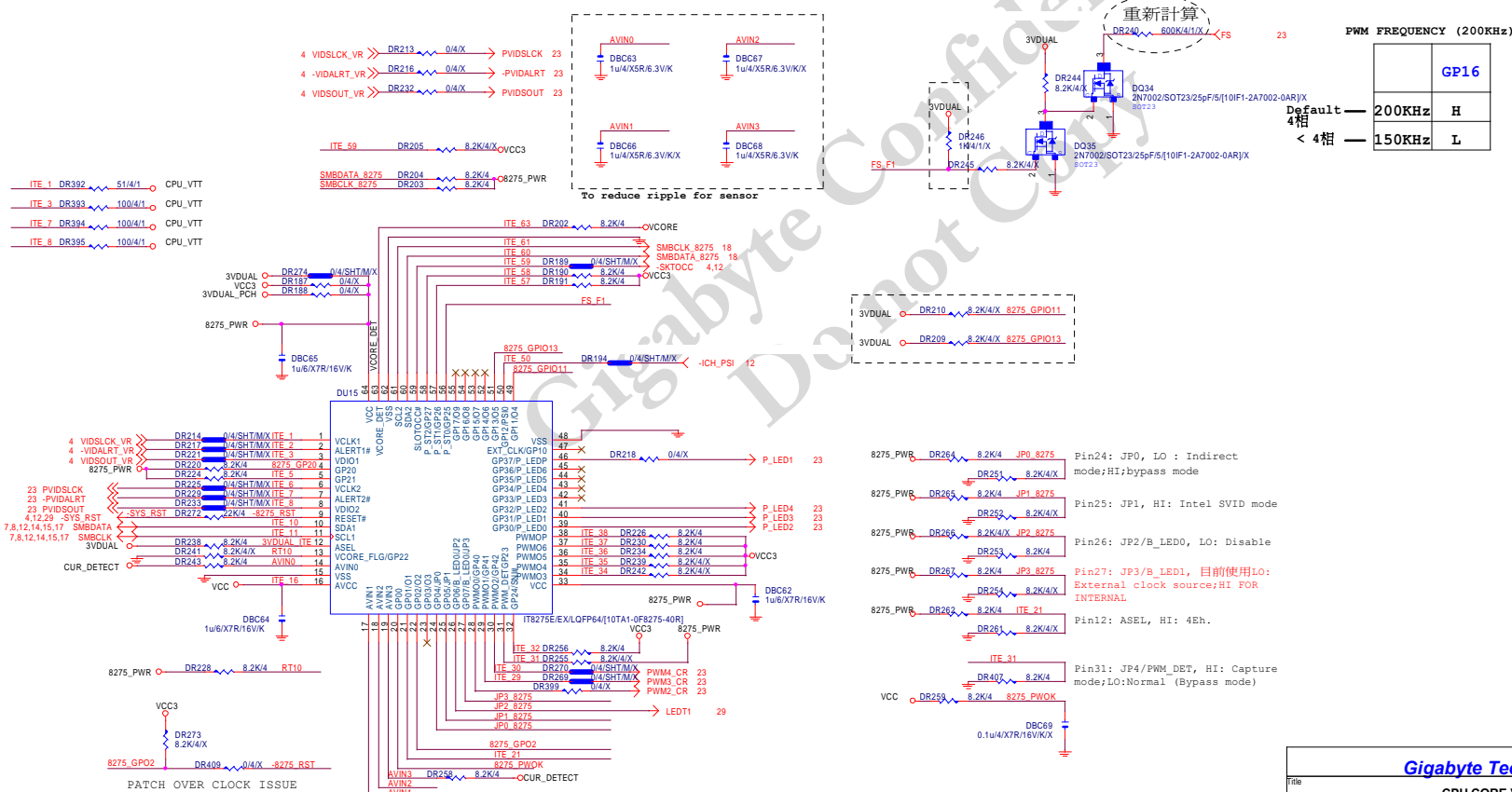
Title				HD AUDIO ALC889			
Size	Document Number	GA-PA65-UD3-B3				Rev	1.0
Custom							
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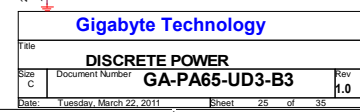




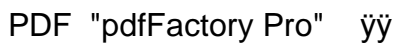
up6262	0X2A	0X20	0X22	0X26
VREF1	DDRVTT	VREF DDRA DQ	VCC1_05_PCH_OV	VCORE
VREF2	VREF DDRA CA	DDR15V	VCC1_8_PCH	VCCSA
VREF3	VREF DDRA CA/VREF DDRB DQ	CPU VTT	SMREF	



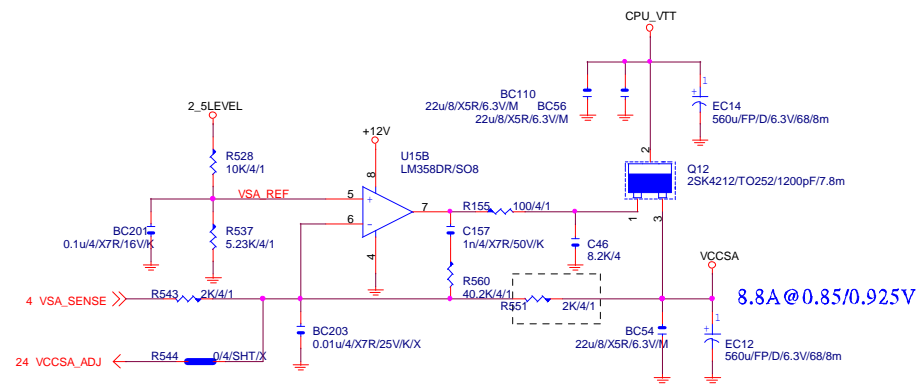
Gigabyte Technology		
CPU CORE VR-2		
GA-PA65-UD3-B3		
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Size	Custom	1.0
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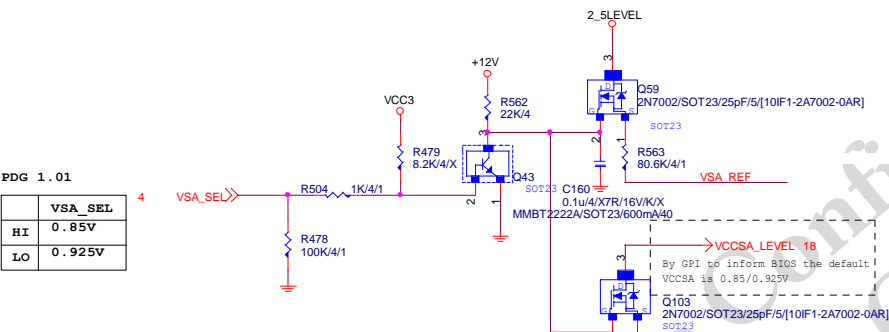


VCC_SA

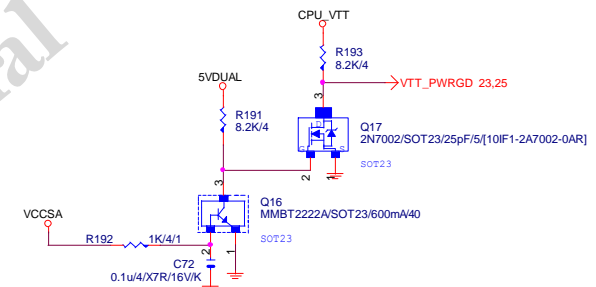
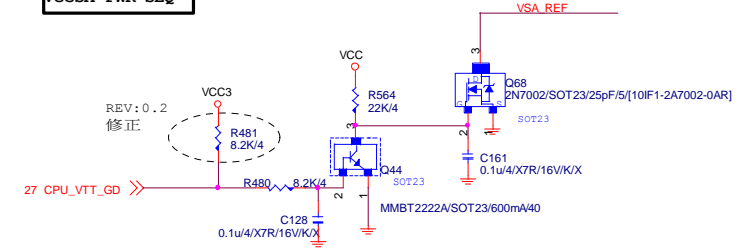


PDG 1.01

VSA_SEL	
HI	0.85V
LO	0.925V

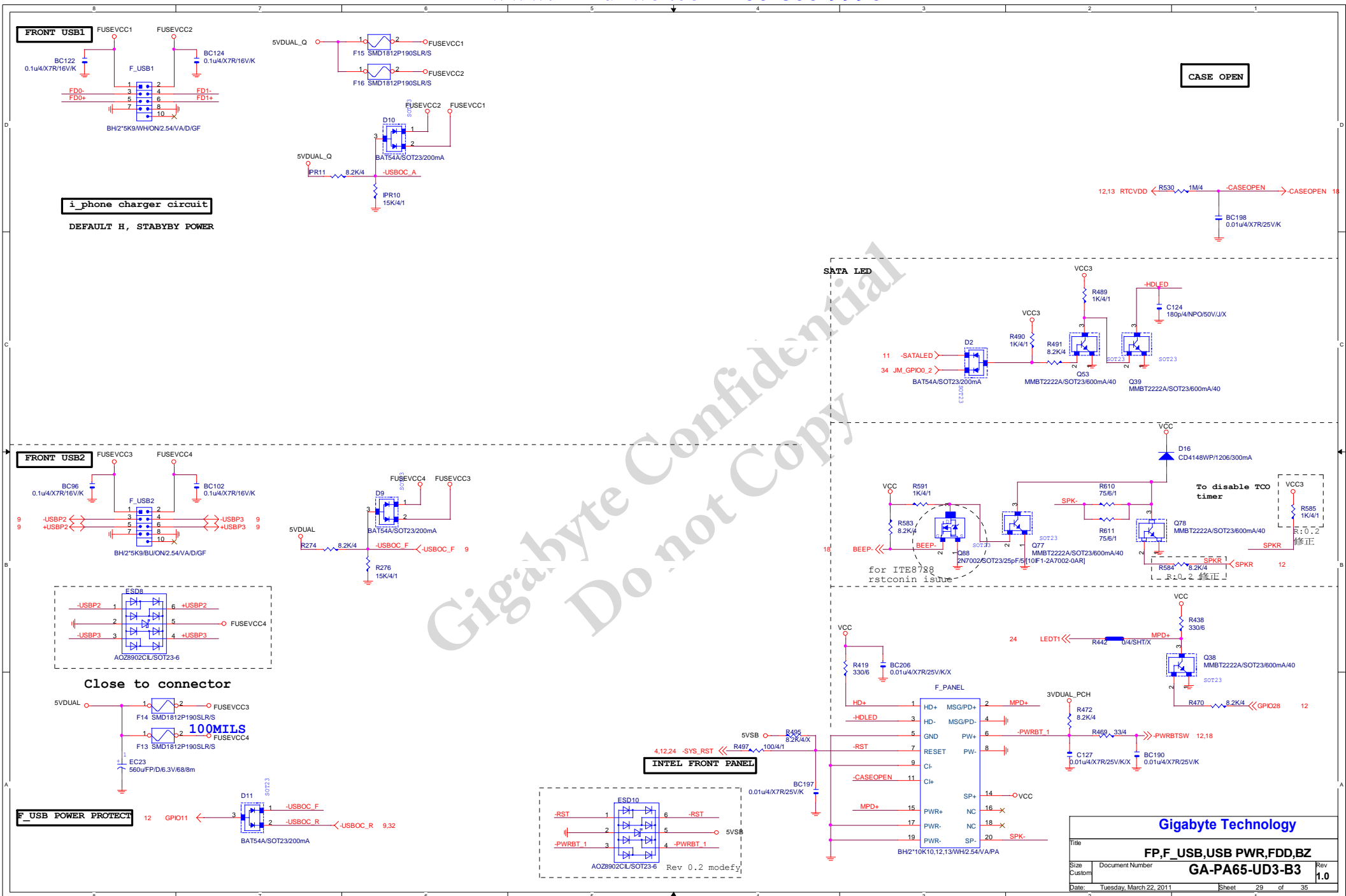


VCCSA_PWR_SEQ

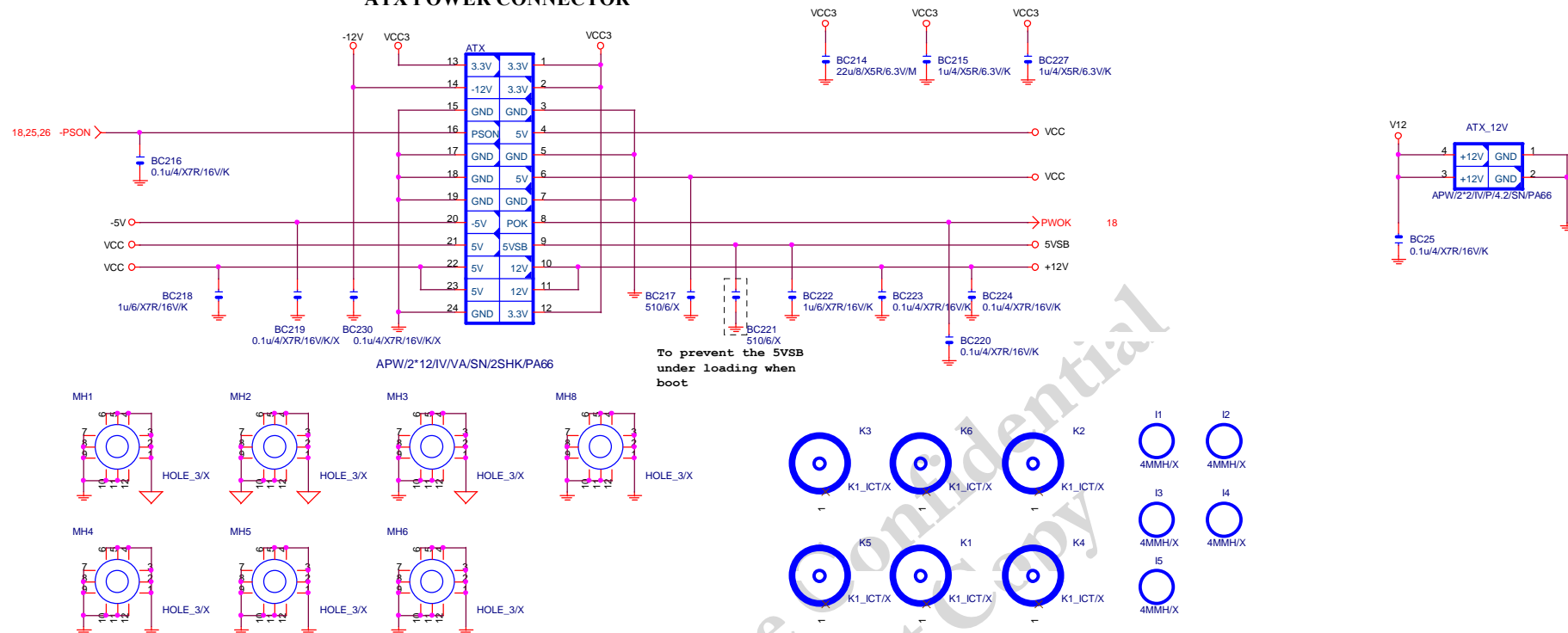


Gigabyte Technology

Title	CPU VTT PWM_ISL6312		
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ATX POWER CONNECTOR



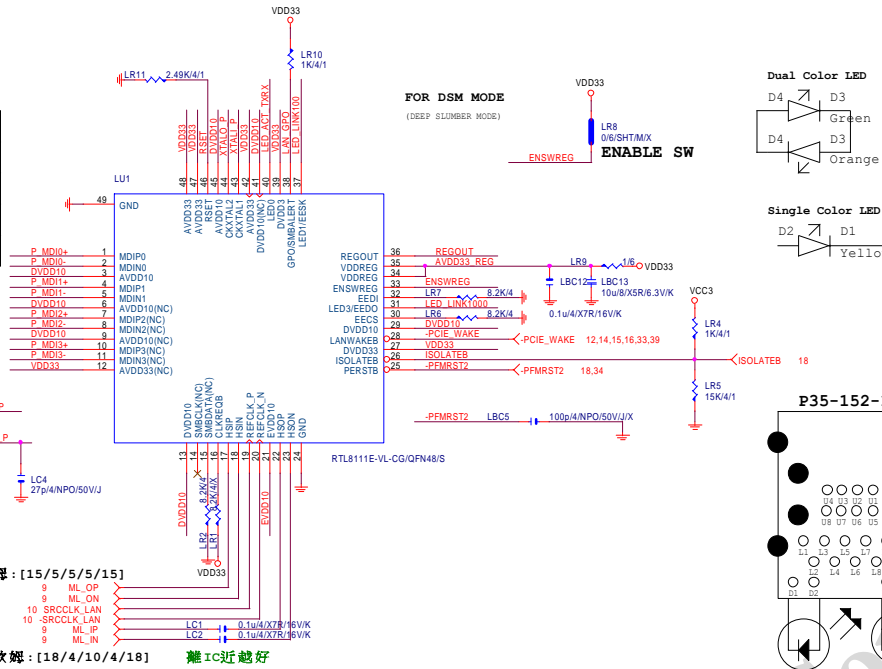
Gigabyte Technology

Title		
ATX POWER CONNECTOR		
Size	Document Number	Rev
Custom	GA-PA65-UD3-B3	1.0
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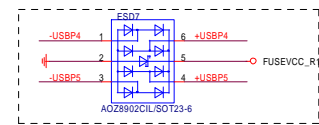
PCIE-1G LAN

Power domain chart

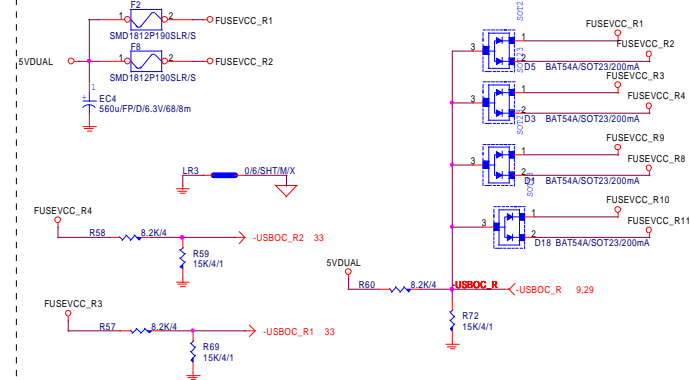
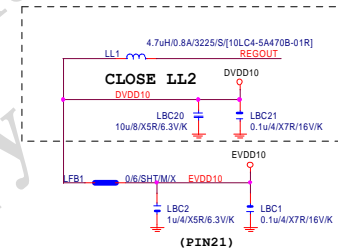
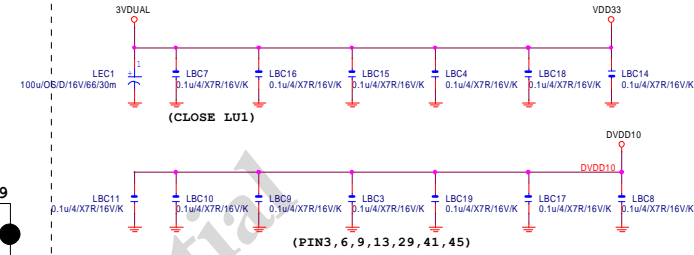
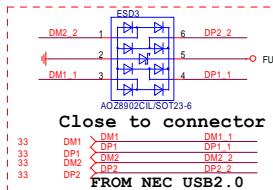
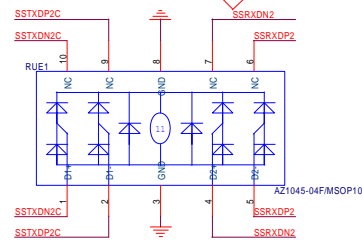
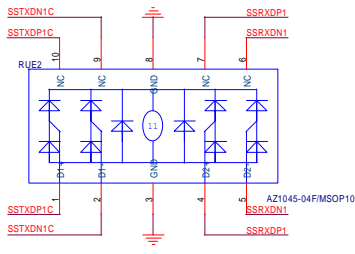
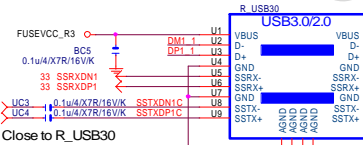
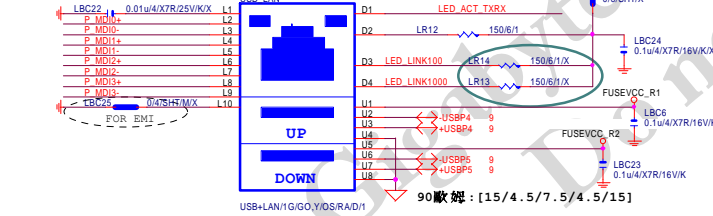
RTL8111E	
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



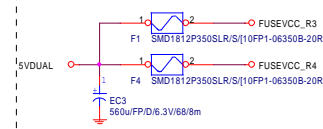
USB30_LAN CONNECTOR



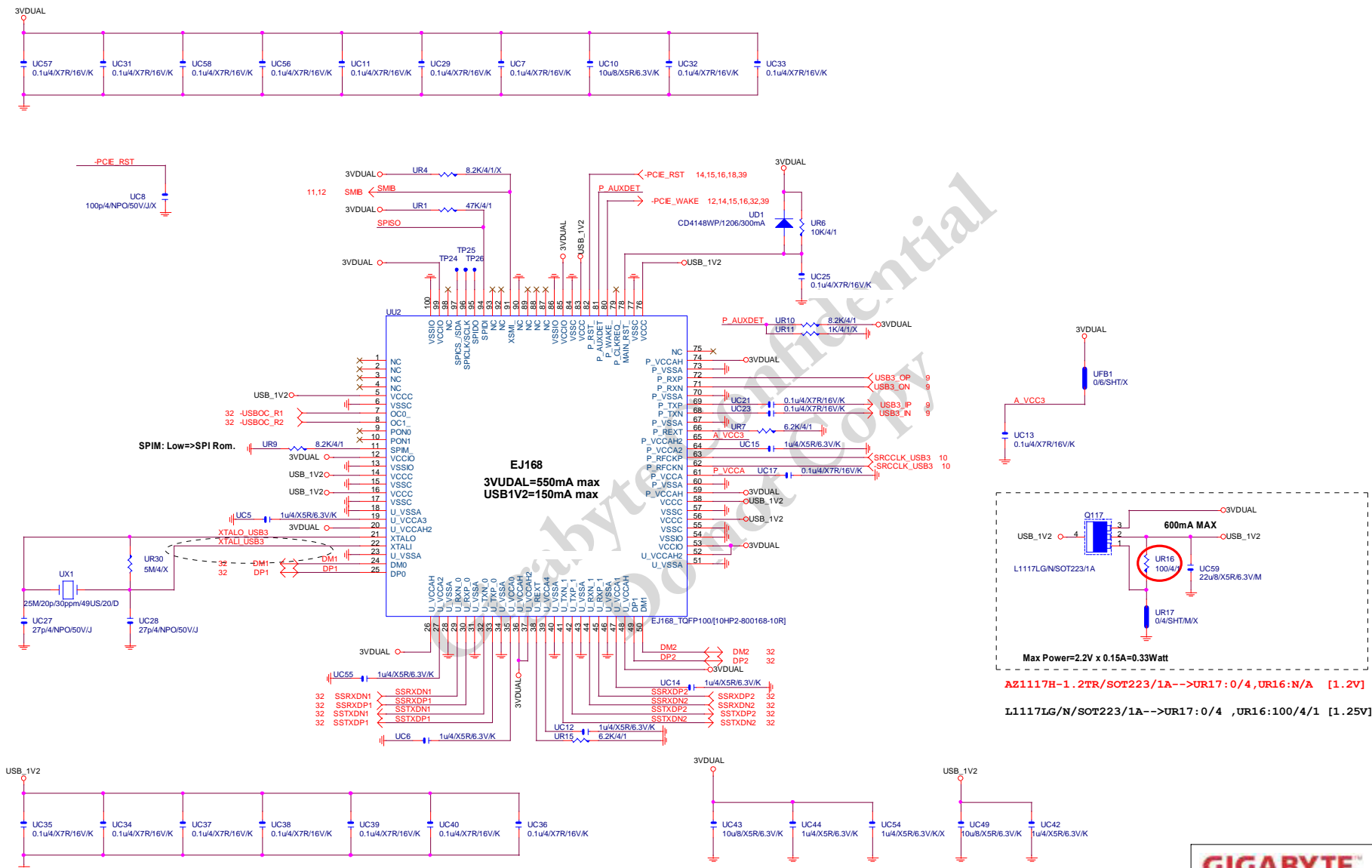
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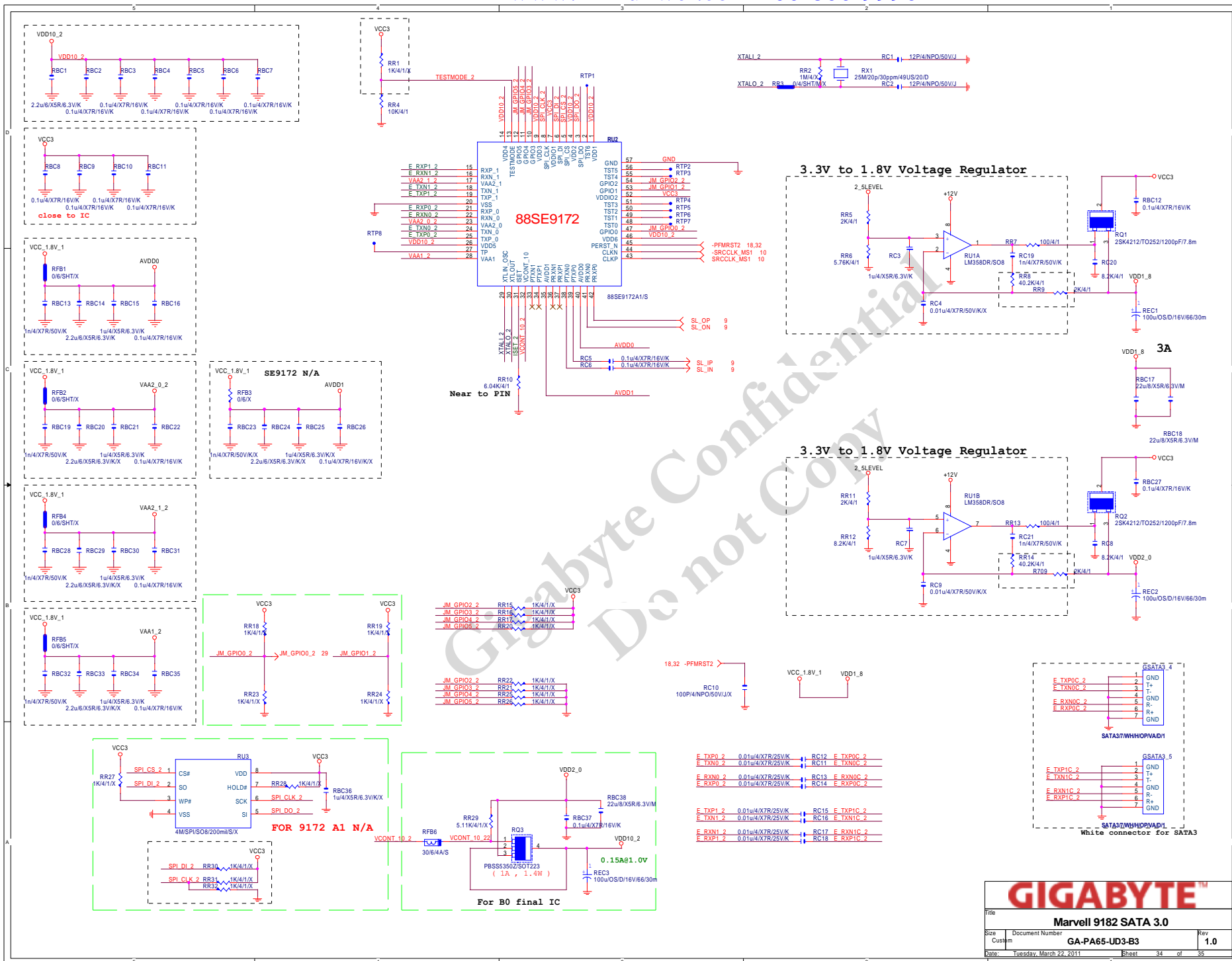
Close to connector



Gigabyte Technology			
REALTEK RTL8111D_1			
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Title REAR USB3.0 E168A		
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GIGABYTE™		
Marvell 9182 SATA 3.0		
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